

# CS315-02 Project 06 Data Mem Sizes

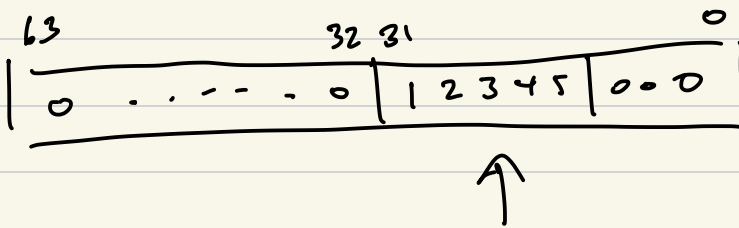
Lab 06 → lui  
Branches

Data Memory

Tests

lui load upper immediate

lui to, 0x12345



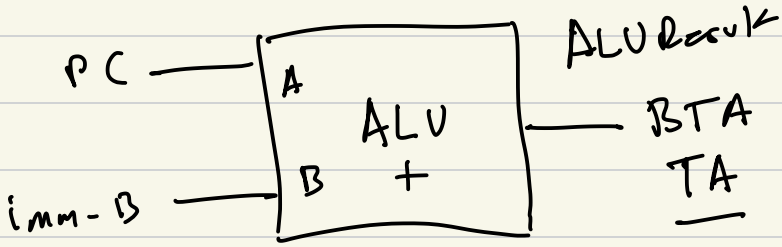
## Branches

1) Calculate BTA (Branch Target Addr)

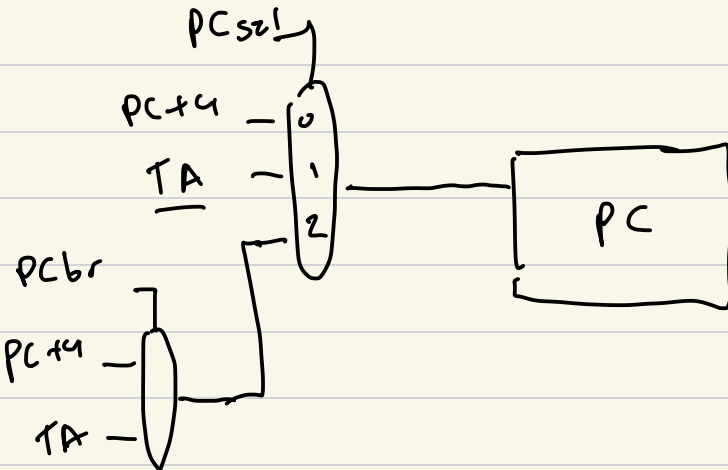
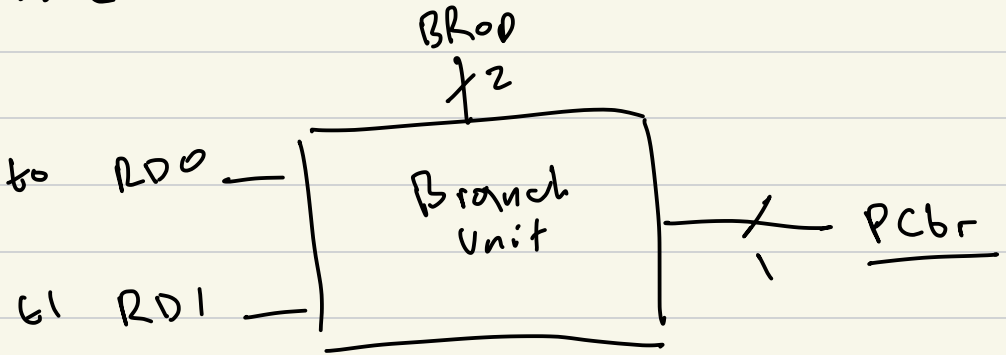
2) Determine if we take the branch

BTA

beg to, t1, label

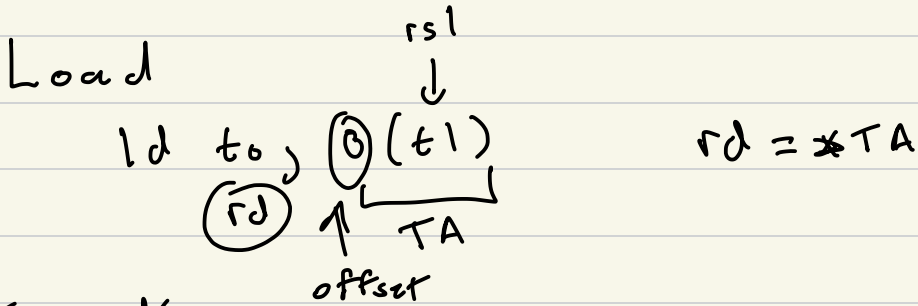


Take branch?

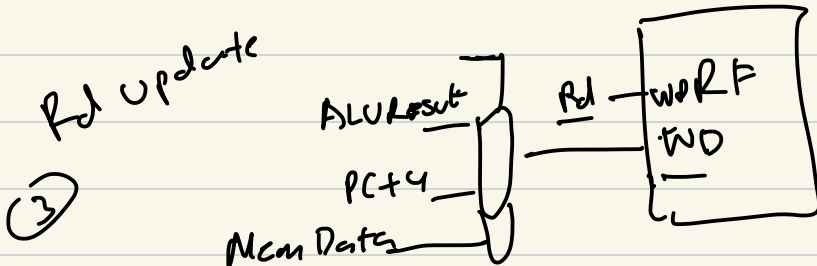
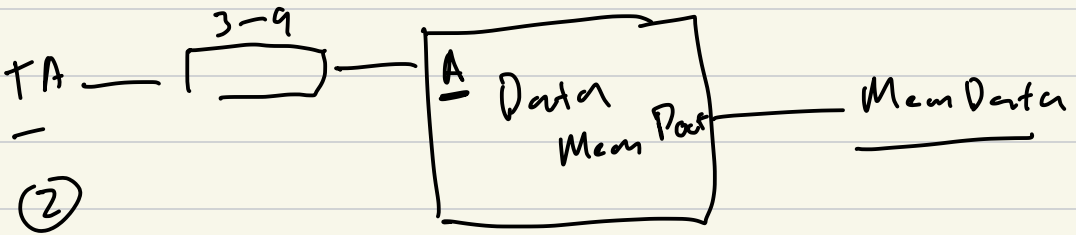
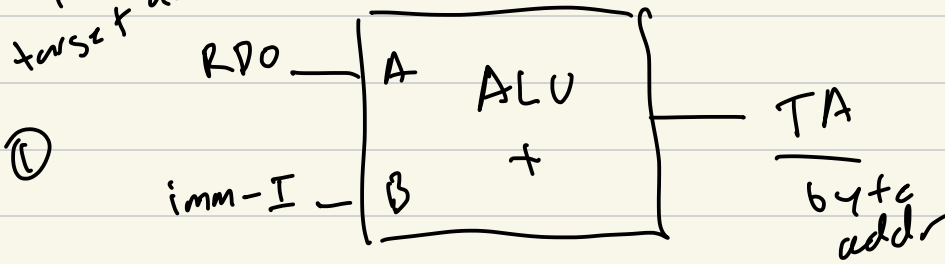


# Data Memory

ld / sd

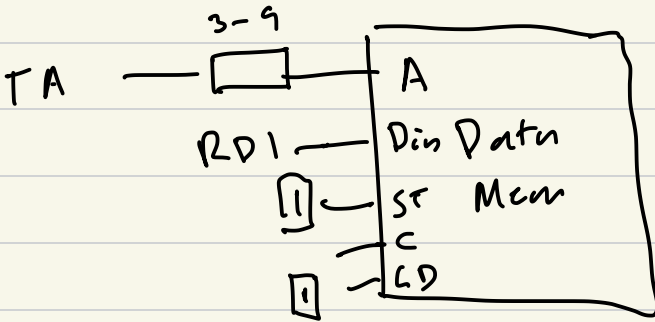
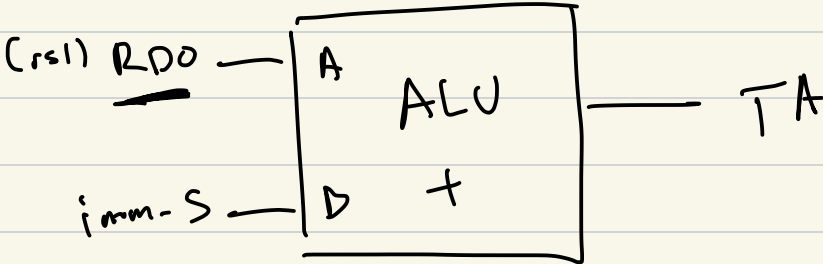


TA  
target addr

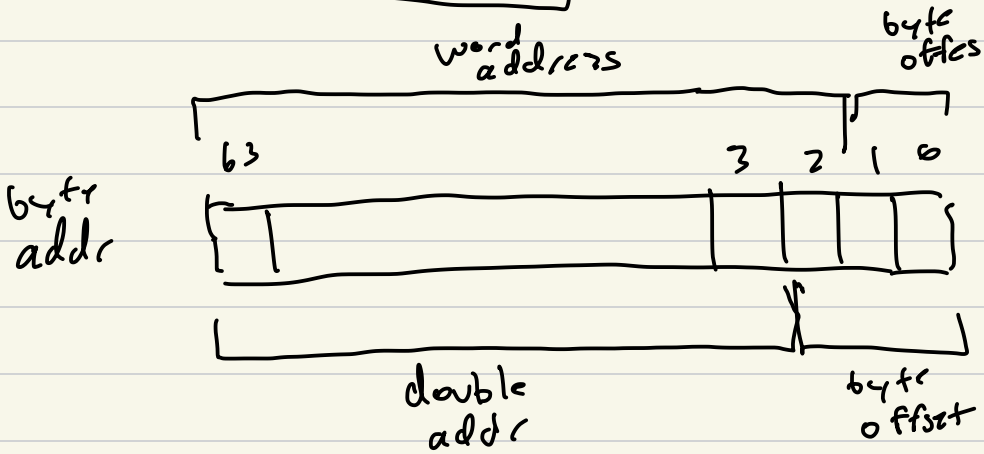
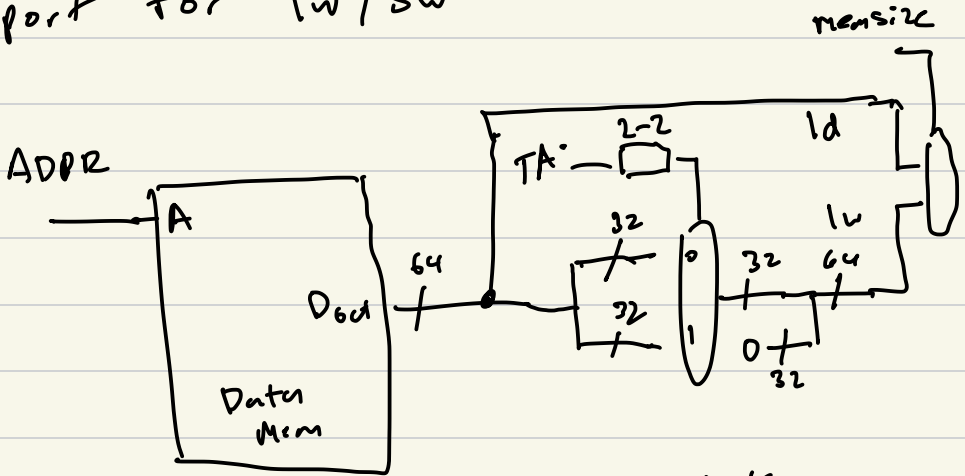


sd

sd to, 0 (tl)  
TA



# Support for lw/sw



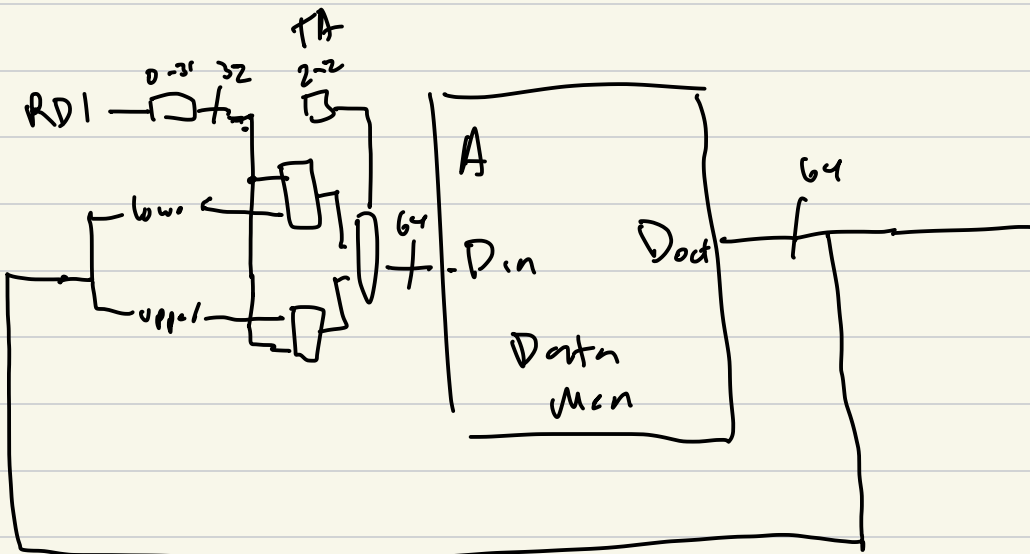


SW

mem[0x(i)] = 0xFFFFFFFF AAAA AAAA

sw to, 0 (E1)

to = 0x77777777



① 0xFFFFFFFF 77777777

② 0x77777777 AAAA AAAA

RMW

Read Modify Write